

Call for Papers



Symposium on Embedded Systems Infrastructure & Theory

as part of the 7th ASME/IEEE International Conference on
Mechatronic and Embedded Systems and Applications

August 29-31, 2011 Washington, DC, USA

<http://iel.ucdavis.edu/mesa/MESA11/>

Objectives: Embedded systems are single-purpose computers built into a larger system for the purposes of controlling and monitoring the system, often with real-time computing constraints. They are usually embedded as part of a complete device including hardware and mechanical parts. The design and validation of reliable systems under environmental and economic constraints poses a lot of research challenges. Manuscripts are solicited in the following topics but not limited to:

- Algorithms for Embedded & Mechatronic Systems
- Models of Embedded Computation
- Software Architectures for Embedded Systems
- Design and Validation of Embedded Systems
- Formal Methods & Case Studies
- System-on-Chip and Network-on-Chip Design
- Hardware/Software Co-Design
- Power-aware Design Issues
- Real-time Aspects & Systems
- Operating System & Middleware Support
- Networked Embedded Systems
- Embedded Systems Applications

Paper Submission: Manuscripts shall be no longer than 10 pages and shall adhere to the [ASME author guidelines](#). Word and L^AT_EX templates are available from [ASME](#) to assist authors in preparing their papers for publication. Final papers in PDF format must be electronically submitted to: <https://www.asmeconferences.org/IDETC2011>

Accepted, peer-reviewed manuscripts will be published in the conference proceedings and EI indexed.

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Important Dates:

Submission of Abstract	11-02-2011
Submission of Full-Length Paper	18-02-2011
Author Notification of Acceptance	20-05-2011
Submission of Copyright Form	27-05-2011
Submission of Final Paper	30-05-2011

Technical Co-Sponsors:

ASME Division of Design Engineering
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