

Call for Papers



Symposium on Design & Verification Tools for Mechatronic & Embedded Systems

as part of the 7th ASME/IEEE International Conference on
Mechatronic and Embedded Systems and Applications
August 29-31, 2011 Washington, DC, USA
<http://iel.ucdavis.edu/mesa/MESA11/>

Objectives: The symposium on Design & Verification Tools for Mechatronic & Embedded Systems is devoted to concepts & tools which assist in the design, implementation and verification process of mechatronic and embedded systems. Topics include, but are not limited to the following:

- System-level design and modelling tools
- Design space exploration tools
- Simulation tools and emulators
- Model checking and equivalence checking tools
- Compilers, assemblers, implementation and synthesis tools
- Power analysis and optimization tools
- Test, debug and diagnosis tools
- Integrated design environments and tool chains
- Hardware/software co-design and co-verification tools
- Surveys, new methodologies and case studies related to design and verification tools for mechatronic & embedded systems

Paper Submission: Manuscripts shall be no longer than 10 pages and shall adhere to the [ASME author guidelines](#). Word and L^AT_EX templates are available from [ASME](#) to assist authors in preparing their papers for publication. Final papers in PDF format must be electronically submitted to: <https://www.asmeconferences.org/IDETC2011>

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Important Dates:

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| Submission of Abstract | 11-02-2011 |
| Submission of Full-Length Paper | 18-02-2011 |
| Author Notification of Acceptance | 20-05-2011 |
| Submission of Copyright Form | 27-05-2011 |
| Submission of Final Paper | 30-05-2011 |

Technical Co-Sponsors:

ASME Division of Design Engineering
IEEE Intelligent Transportation Systems Society
IEEE Control Systems Society

